IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	
Richard L. Coulson et al.	§	Art Unit: 2863
	§	
Serial No.: 10/660,310	§	Examiner: Aditya S. Bhat
	§	
Filed: September 11, 2003	§	Docket: ITL.1029US
	§	P16765
For: Adaptive Cache Algorithm for	§	
Temperature Sensitive Memory	§	Assignee: Intel Corporation
	§	

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO PAPER NO. 20041208

Sir:

In response to the office action mailed December 14, 2004, reconsideration is requested in view of the following remarks.